

Appl. No. 10/572,612
Amendment and/or Response
Reply to Office action of 3 October 2006

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Amendments to the Specification:

Please delete the Abstract of the invention in its entirety, and add the following new Abstract:

~~The invention relates to an~~An electronic circuit for providing a logic gate function
includes a ~~comprising~~ differential signal input means, a combining stage, a
discriminating stage and a differential signal output means. The discriminating stage
~~comprises~~includes four transistors (~~Q8, Q9, Q10, Q11~~) each having first electrodes
(~~83, 93, 103, 113~~) and second electrodes (~~81, 91, 101, 111~~) and a respective gate
electrode (~~82, 92, 102, 112~~). The first electrodes of said ~~the~~ four transistors are
connected to a common node. The combining stage is arranged to convert
differential input signals into gate signals applied to the grating gate electrodes of
some of ~~said the~~ four transistors respectively.

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